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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of:	Aronson et al.	Confirmation No.:	2923
Serial No.:	10/713,685	Art Unit:	2633
Filed:	November 13, 2003	Examiner:	Leung, Christina Y.
For:	<i>Integrated Memory Mapped Controller Circuit for Fiber Optics Transceiver</i>	Attorney Docket No.:	060900-0207-US

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure provisions of 37 C.F.R. §1.56, there is hereby provided certain information which the Examiner may consider material to the examination of the subject U.S. patent application. It is requested that the Examiner make this information of record if it is deemed material to the examination of the application.

1. Enclosures accompanying this Information Disclosure Statement are:

- 1a. ☒ A list of all patents, publications, applications, or other information submitted for consideration by the office.
- 1b. A legible copy of :
  - ☐ Each U.S. patent application publication and U.S. and foreign patent;
  - ☒ Each publication or that portion which caused it to be listed on the PTO-1449;
  - ☐ For each cited pending U.S. application, the application specification including the claims, and any drawing of the application, or portion of the application which caused it to be listed on the PTO-1449 including any claims directed to that portion;
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- 1c. ☒ An English language copy of search report(s) from a counterpart foreign application or PCT International Search Report.
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